



#3/ Cited Art  
G. Hawkins  
11/89

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Rule 53(b) Divisional Application of:

T. AKAMATSU et al

Serial No.: Div. of 08/769,529

Group Art Unit: Unassigned

Filed: Herewith

Examiner: Unassigned

For: INTEGRATED ELECTRONIC DEVICE HAVING FLIP-CHIP CONNECTION WITH  
CIRCUIT BOARD AND FABRICATION METHOD THEREOF

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Assistant Commissioner for Patents  
Washington, D.C. 20231

September 9, 1999

Sir:

This Information Disclosure Statement is being filed in order to comply with applicants' duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 08/769,529, filed December 19, 1996.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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